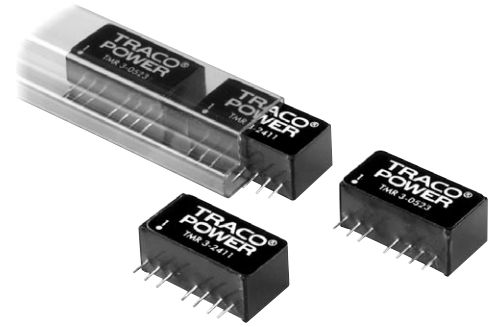


DC/DC Converters

- ◆ Highest Power Density in SIP-Package
- ◆ Wide 2:1 Input Range
- ◆ Regulated Outputs
- ◆ I/O Isolation 1000 VDC
- ◆ Operating Temperature Range $-40\text{ }^{\circ}\text{C}$ to $+75\text{ }^{\circ}\text{C}$
- ◆ Low Ripple and Noise
- ◆ Short Circuit Protection
- ◆ Remote On/Off

TMR 3 Series, 3 Watt

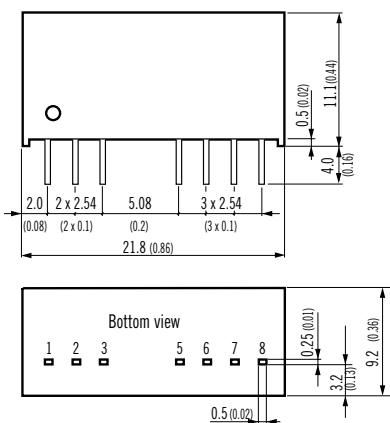


NEW
Product

Models

Order code	Input voltage	Output voltage	Output current max.
TMR 3-0510	4.5 – 9.0 VDC	3.3 VDC	700 mA
TMR 3-0511		5 VDC	600 mA
TMR 3-0512		12 VDC	250 mA
TMR 3-0513		15 VDC	200 mA
TMR 3-0521		± 5 VDC	± 200 mA
TMR 3-0522		± 12 VDC	± 125 mA
TMR 3-0523		± 15 VDC	± 100 mA
TMR 3-1210	9 – 18 VDC	3.3 VDC	700 mA
TMR 3-1211		5 VDC	600 mA
TMR 3-1212		12 VDC	250 mA
TMR 3-1213		15 VDC	200 mA
TMR 3-1221		± 5 VDC	± 200 mA
TMR 3-1222		± 12 VDC	± 125 mA
TMR 3-1223		± 15 VDC	± 100 mA
TMR 3-2410	18 – 36 VDC	3.3 VDC	700 mA
TMR 3-2411		5 VDC	600 mA
TMR 3-2412		12 VDC	250 mA
TMR 3-2413		15 VDC	200 mA
TMR 3-2421		± 5 VDC	± 200 mA
TMR 3-2422		± 12 VDC	± 125 mA
TMR 3-2423		± 15 VDC	± 100 mA
TMR 3-4810	36 – 75 VDC	3.3 VDC	700 mA
TMR 3-4811		5 VDC	600 mA
TMR 3-4812		12 VDC	250 mA
TMR 3-4813		15 VDC	200 mA
TMR 3-4821		± 5 VDC	± 200 mA
TMR 3-4822		± 12 VDC	± 125 mA
TMR 3-4823		± 15 VDC	± 100 mA

- Line regulation:** $\pm 0.2\%$ max.
- Load regulation:**
 - Single output models: $\pm 0.5\%$ max.
 - Dual output models: $\pm 1.0\%$ max.
- Ripple & Noise:** < 75 mVpk-pk (20 MHz BW)
(no external output capacitor required)
- Short circuit protection:** continuous, automatic recovery
- Efficiency:** 79 % typ.
- Operating temperature range:** $-40\text{ }^{\circ}\text{C}$... $+75\text{ }^{\circ}\text{C}$ (no derating)
- I/O isolation voltage:** 1000 VDC
- Case:** plastic (UL94 V-0 rating)
- Remote On/Off:** shutdown input for low input current (1 mA) in standby operation



() = Inches

Pin-Out		
Pin	Single Output	Dual Output
1	– Vin (GND)	– Vin (GND)
2	+ Vin (Vcc)	+ Vin (Vcc)
3	Remote On/Off	Remote On/Off
5	No con.	No con.
6	+ Vout	+ Vout
7	– Vout	Common
8	No con.	– Vout